

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	22560	memory adj module	US-PGPUB; USPAT	OR	ON	2006/07/23 11:35
L2	1690567	group	US-PGPUB; USPAT	OR	ON	2006/07/23 11:35
L3	1630	1 same 2	US-PGPUB; USPAT	OR	ON	2006/07/23 11:36
L4	12201	chip adj select	US-PGPUB; USPAT	OR	ON	2006/07/23 11:36
L5	224	3 and 4	US-PGPUB; USPAT	OR	ON	2006/07/23 11:36
L6	17	park-myun-joo.in.	US-PGPUB; USPAT	OR	ON	2006/07/23 11:36
L7	50	so-byung-se.in.	US-PGPUB; USPAT	OR	ON	2006/07/23 11:36
L8	27	lee-jae-jun.in.	US-PGPUB; USPAT	OR	ON	2006/07/23 11:36

Day : Sunday  
Date: 7/23/2006

Time: 11:38:58



**PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = PARK

First Name = MYUN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09799094</a>	<a href="#">6815621</a>	150	03/06/2001	CHIP SCALE PACKAGE, PRINTED CIRCUIT BOARD, AND METHOD OF DESIGNING A PIINTED CIRCUIT BOARD	PARK, MYUN JOO
<a href="#">10471373</a>	Not Issued	41	09/09/2003	Door and a drum type washing machine and a clothes dryer using the same	PARK, MYUN SIK
<a href="#">09777446</a>	<a href="#">6772262</a>	150	02/06/2001	MEMORY MODULE WITH IMPROVED DATA BUS PERFORMANCE	PARK, MYUN-JOO
<a href="#">09777547</a>	<a href="#">6414904</a>	150	02/06/2001	TWO CHANNEL MEMORY SYSTEM HAVING SHARED CONTROL AND ADDRESS BUS AND MEMORY MODULES USED THEREFOR	PARK, MYUN-JOO
<a href="#">09851277</a>	Not Issued	41	05/08/2001	Memory interface systems that couple a memory to a memory controller and are responsive to a terminal voltage that is independent of supply voltages for the memory and the memory controller	PARK, MYUN-JOO
<a href="#">09858401</a>	<a href="#">6480409</a>	150	05/16/2001	MEMORY MODULES HAVING INTEGRAL TERMINATING RESISTORS AND COMPUTER SYSTEM BOARDS FOR USE WITH SAME	PARK, MYUN-JOO
<a href="#">10200731</a>	<a href="#">6870742</a>	150	07/22/2002	SYSTEM BOARD	PARK, MYUN-JOO
<a href="#">10353924</a>	<a href="#">6828819</a>	150	01/30/2003	HIGH-SPEED MEMORY SYSTEM	PARK, MYUN-JOO
<a href="#">10424923</a>	Not Issued	61	04/29/2003	Semiconductor memory device with data bus scheme for reducing high frequency noise	PARK, MYUN-JOO

<u>10629866</u>	Not Issued	71	07/30/2003	Memory system having memory modules with different memory device loads	PARK, MYUN-JOO
<u>10644735</u>	Not Issued	71	08/21/2003	Semiconductor memory system having multiple system data buses	PARK, MYUN-JOO
<u>10883488</u>	<u>6990543</u>	150	07/01/2004	MEMORY MODULE WITH IMPROVED DATA BUS PERFORMANCE	PARK, MYUN-JOO
<u>08482393</u>	<u>5577039</u>	150	06/07/1995	SYSTEM AND METHOD OF SIGNAL TRANSMISSION WITHIN A PLESIOCHRONOUS DIGITAL HIERARCHY UNIT USING ATM ADAPTATION LAYERS	PARK, MYUNG C.
<u>11429005</u>	Not Issued	20	05/05/2006	Method and apparatus for performing an instant messenger function using a local area network	PARK, MYUNG EUN
<u>09950478</u>	<u>6677181</u>	150	09/10/2001	STACK PACKAGE AND METHOD FOR FABRICATING THE SAME	PARK, MYUNG GEUN
<u>11291612</u>	Not Issued	20	12/01/2005	Stack type package module and method for manufacturing the same	PARK, MYUNG GEUN
<u>09309399</u>	<u>6316825</u>	150	05/10/1999	CHIP STACK PACKAGE UTILIZING A CONNECTING HOLE TO IMPROVE ELECTRICAL CONNECTION BETWEEN LEADFRAMES	PARK, MYUNG GEUN
<u>08843738</u>	Not Issued	161	04/21/1997	HIGH EFFICIENCY PROCESS AND APPARATUS FOR TREATING NITROGEN AND PHOSPHOROUS CONTAINED IN SEWAGE	PARK, MYUNG GYUN
<u>09164752</u>	<u>6177007</u>	150	10/01/1998	HIGH EFFICIENCY APPARATUS FOR TREATING NITROGEN AND PHOSPHOROUS CONTAINED IN SEWAGE	PARK, MYUNG GYUN
<u>08300453</u>	Not Issued	166	09/02/1994	CRT ELECTRON GUN FOR CONTROLLING DIVERGENCE ANGLE OF ELECTRON BEAMS ACCORDING TO INTENSITY OF CURRENT	PARK, MYUNG H
<u>06637569</u>	Not Issued	161	08/03/1984	LONG-LIFE INCANDESCENT LAMP OF IMPROVED	PARK, MYUNG H.

				OPERATIONAL SAFETY	
<u>07598887</u>	Not Issued	166	10/19/1990	QUINOLONE COMPOUNDS AND A PROCESS FOR THEIR PREPARATION	PARK, MYUNG H.
<u>07598888</u>	<u>5089621</u>	150	10/19/1990	DIAZABICYCLO AMINE COMPOUNDS WHICH ARE INTERMEDIATES FOR ANTI-BACTERIAL COMPOUNDS	PARK, MYUNG H.
<u>07599225</u>	<u>5091384</u>	150	10/19/1990	ANTI-BACTERIAL QUINOLONE- AND NAPHTHYRIDONE-CARBOXYLIC ACID COMPOUNDS	PARK, MYUNG H.
<u>07905185</u>	Not Issued	164	06/26/1992	ANTI-BACTERIAL PYRIDO [1,2,3-DE]-1,4-BENZOXAZINES	PARK, MYUNG H.
<u>07997961</u>	Not Issued	161	12/29/1992	NOVEL QUINOLONE CARBOXYLIC ACID DERIVATIVES AND PROCESSES FOR PREPARING SAME	PARK, MYUNG H.
<u>07997962</u>	Not Issued	161	12/29/1992	NOVEL AZASPIRO DERIVATIVES AND PROCESSES FOR PREPARING SAME	PARK, MYUNG H.
<u>07998231</u>	<u>5344846</u>	150	12/30/1992	COMPOSITIONS AND METHODS FOR INHIBITING DEOXYHYPUSINE SYNTHASE AND THE GROWTH OF CELLS	PARK, MYUNG H.
<u>08073207</u>	<u>5409933</u>	250	06/08/1993	NOVEL QUINOLINE DERIVATIVES AND PROCESSES FOR PREPARING THE SAME	PARK, MYUNG H.
<u>08187125</u>	Not Issued	161	01/27/1994	NOVEL QUINOLONE CARBOXYLIC ACID DERIVATIVES AND PROCESSES FOR PREPARING SAME	PARK, MYUNG H.
<u>08188128</u>	Not Issued	161	01/28/1994	NOVEL AZASPIRO DERIVATIVES AND PROCESSES FOR PREPARING SAME	PARK, MYUNG H.
<u>08349445</u>	Not Issued	166	12/05/1994	ELECTRON GUNS FOR COLOR PICTURE TUBE	PARK, MYUNG H.
<u>08756589</u>	<u>5710481</u>	150	11/27/1996	CRT ELECTRON GUN FOR CONTROLLING DIVERGENCE ANGLE OF ELECTRON BEAMS	PARK, MYUNG H.

				ACCORDING TO INTENSITY OF CURRENT	
<u>60314561</u>	Not Issued	159	08/23/2001	Composition and method for inhibiting angiogenesis and cell proliferation	PARK, MYUNG HEE
<u>60748879</u>	Not Issued	20	12/09/2005	Deoxyhypusine hydroxylase	PARK, MYUNG HEE
<u>09714429</u>	<u>6624799</u>	150	11/17/2000	RADIO FREQUENCY PLASMA DISPLAY PANEL	PARK, MYUNG HO
<u>11354036</u>	Not Issued	20	02/15/2006	Electroless metal film-plating system	PARK, MYUNG HO
<u>08711063</u>	Not Issued	166	09/09/1996	ELECTRON GUNS FOR COLOR PICTURE TUBE	PARK, MYUNG HO
<u>08890283</u>	<u>5736812</u>	150	07/09/1997	ELECTRON GUNS FOR COLOR PICTURE TUBE	PARK, MYUNG HO
<u>08950975</u>	<u>6013309</u>	150	10/15/1997	PROTECTION LAYER OF PLASMA DISPLAY PANEL AND METHOD OF FORMING THE SAME	PARK, MYUNG HO
<u>09340045</u>	<u>6473061</u>	150	06/28/1999	PLASMA DISPLAY PANEL DRIVE METHOD AND APPARATUS	PARK, MYUNG HO
<u>09442747</u>	<u>6379783</u>	150	11/18/1999	PROTECTION LAYER OF PLASMA DISPLAY PANEL AND METHOD OF FORMING THE SAME	PARK, MYUNG HO
<u>10482782</u>	Not Issued	41	08/24/2004	Method for processing ginseng and the uses of extract of processed ginseng	PARK, MYUNG HWAN
<u>07997963</u>	Not Issued	161	12/29/1992	NOVEL QUINOLONE CARBOXYLIC ACID DERIVATIVES AND PROCESSES FOR PREPARING SAME	PARK, MYUNG HWAN
<u>11119149</u>	Not Issued	30	04/29/2005	Semiconductor nano-structure and method of forming the same	PARK, MYUNG IL
<u>07302468</u>	<u>5026955</u>	150	01/27/1989	DRIVE CONTROL METHOD FOR AN ELECTROMAGNETIC INDUCTION HEATING APPARATUS	PARK, MYUNG JONG
<u>06829908</u>	Not Issued	161	02/18/1986	DRIVING CIRCUIT OF THE INVERTER FOR PULSATING CURRENT SOURCE-TYPE DISCHARGE LAMP	PARK, MYUNG K.
<u>06857378</u>	Not	161	04/30/1986	HIGH POWER FACTOR	PARK, MYUNG K.

	Issued			RECTIFYING CIRCUIT	
<u>07030678</u>	Not Issued	161	03/26/1987	HIGH POWER FACTOR BALLAST CIRCUIT OPERATING UNDER PULSATING DC POTENTIAL	PARK, MYUNG K.
<u>10499703</u>	Not Issued	30	06/21/2004	Fuel cell	PARK, MYUNG SEOK

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**Search Another: Inventor**

Last Name	First Name
<input type="text" value="park"/>	<input type="text" value="myun"/>

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Date: 7/23/2006

Time: 11:39:24



## Inventor Name Search Result

Your Search was:

Last Name = SO

First Name = BYUNG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09799094</u>	<u>6815621</u>	150	03/06/2001	CHIP SCALE PACKAGE, PRINTED CIRCUIT BOARD, AND METHOD OF DESIGNING A PIINTED CIRCUIT BOARD	SO, BYUNG SE
<u>08773787</u>	<u>5856982</u>	150	12/24/1996	HIGH-SPEED DISTURB TESTING METHOD AND WORD LINE DECODER IN SEMICONDUCTOR MEMORY DEVICE	SO, BYUNG-SE
<u>09426609</u>	<u>6714595</u>	150	10/26/1999	SIGNAL TRANSMISSION CIRCUITS THAT USE MULTIPLE INPUT SIGNALS TO GENERATE A RESPECTIVE TRANSMIT SIGNAL AND METHODS OF OPERATING THE SAME	SO, BYUNG-SE
<u>09454339</u>	<u>6587976</u>	150	12/03/1999	SEMICONDUCTOR DEVICE TESTER FOR MEASURING SKEW BETWEEN OUTPUT PINS OF A SEMICONDUCTOR DEVICE	SO, BYUNG-SE
<u>09540988</u>	<u>6252805</u>	150	03/31/2000	Semiconductor memory device including programmable output pin determining unit and method of reading the same during test mode	SO, BYUNG-SE
<u>09612610</u>	<u>6382986</u>	150	07/08/2000	Socket for mounting memory module boards on a printed circuit board	SO, BYUNG-SE
<u>09688297</u>	<u>6632705</u>	150	10/13/2000	MEMORY MODULES AND PACKAGES USING DIFFERENT ORIENTATIONS AND TERMINAL ASSIGNMENTS	SO, BYUNG-SE

<u>09777446</u>	<u>6772262</u>	150	02/06/2001	MEMORY MODULE WITH IMPROVED DATA BUS PERFORMANCE	SO, BYUNG-SE
<u>09777547</u>	<u>6414904</u>	150	02/06/2001	TWO CHANNEL MEMORY SYSTEM HAVING SHARED CONTROL AND ADDRESS BUS AND MEMORY MODULES USED THEREFOR	SO, BYUNG-SE
<u>09851277</u>	Not Issued	41	05/08/2001	Memory interface systems that couple a memory to a memory controller and are responsive to a terminal voltage that is independent of supply voltages for the memory and the memory controller	SO, BYUNG-SE
<u>09858401</u>	<u>6480409</u>	150	05/16/2001	MEMORY MODULES HAVING INTEGRAL TERMINATING RESISTORS AND COMPUTER SYSTEM BOARDS FOR USE WITH SAME	SO, BYUNG-SE
<u>10043047</u>	Not Issued	41	01/09/2002	Memory system having stub bus configuration	SO, BYUNG-SE
<u>10074309</u>	<u>6754112</u>	150	02/11/2002	INTEGRATED CIRCUIT DEVICES HAVING DELAY CIRCUITS FOR CONTROLLING SETUP/DELAY TIMES OF DATA SIGNALS THAT ARE PROVIDED TO MEMORY DEVICES AND METHODS OF OPERATING SAME	SO, BYUNG-SE
<u>10094448</u>	<u>6944737</u>	150	03/08/2002	MEMORY MODULES AND METHODS HAVING A BUFFER CLOCK THAT OPERATES AT DIFFERENT CLOCK FREQUENCIES ACCORDING TO THE OPERATING MODE	SO, BYUNG-SE
<u>10200731</u>	<u>6870742</u>	150	07/22/2002	SYSTEM BOARD	SO, BYUNG-SE
<u>10353924</u>	<u>6828819</u>	150	01/30/2003	HIGH-SPEED MEMORY SYSTEM	SO, BYUNG-SE
<u>10629866</u>	Not Issued	71	07/30/2003	Memory system having memory modules with different memory device loads	SO, BYUNG-SE
<u>10644735</u>	Not Issued	71	08/21/2003	Semiconductor memory system having multiple system data buses	SO, BYUNG-SE



<u>10722159</u>	Not Issued	80	11/26/2003	Multi-chip package for reducing parasitic load of pin	SO, BYUNG-SE
<u>10794680</u>	7049849	150	03/05/2004	SIGNAL TRANSMISSION CIRCUITS THAT USE MULTIPLE INPUT SIGNALS TO GENERATE A RESPECTIVE TRANSMIT SIGNAL	SO, BYUNG-SE
<u>10795507</u>	6836138	150	03/09/2004	MODULE HAVING TEST ARCHITECTURE FOR FACILITATING THE TESTING OF BALL GRID ARRAY PACKAGES, AND TEST METHOD USING THE SAME	SO, BYUNG-SE
<u>10831702</u>	Not Issued	41	04/23/2004	Memory module and method of testing the same	SO, BYUNG-SE
<u>10833322</u>	Not Issued	30	04/28/2004	Buffered memory module and method for testing same	SO, BYUNG-SE
<u>10837610</u>	Not Issued	83	05/04/2004	Memory system with improved signal integrity	SO, BYUNG-SE
<u>10853353</u>	7072201	150	05/26/2004	MEMORY MODULE	SO, BYUNG-SE
<u>10883488</u>	6990543	150	07/01/2004	MEMORY MODULE WITH IMPROVED DATA BUS PERFORMANCE	SO, BYUNG-SE
<u>10900140</u>	Not Issued	30	07/28/2004	Memory module test system	SO, BYUNG-SE
<u>10913359</u>	Not Issued	30	08/09/2004	Data transmission system and method	SO, BYUNG-SE
<u>10975810</u>	Not Issued	20	10/27/2004	Memory module with registers	SO, BYUNG-SE
<u>10988390</u>	Not Issued	41	11/12/2004	Mounting structure in integrated circuit module	SO, BYUNG-SE
<u>10997406</u>	Not Issued	41	11/24/2004	Memory module system with efficient control of on-die termination	SO, BYUNG-SE
<u>11029008</u>	Not Issued	71	01/05/2005	Memory module, memory unit, and hub with non-periodic clock and methods of using the same	SO, BYUNG-SE
<u>11064671</u>	Not Issued	93	02/24/2005	MEMORY MODULE AND A METHOD OF ARRANGING A SIGNAL LINE OF THE SAME	SO, BYUNG-SE
<u>11102181</u>	Not Issued	30	04/08/2005	Memory module with memory devices of different capacity	SO, BYUNG-SE
<u>11118377</u>	Not Issued	20	05/02/2005	Method of testing a memory module and hub of the memory	SO, BYUNG-SE

				module	
<u>11177736</u>	Not Issued	30	07/08/2005	Stacked board-on-chip package having mirroring structure and dual inline memory module on which the stacked board-on-chip packages are mounted	SO, BYUNG-SE
<u>11181059</u>	Not Issued	30	07/13/2005	Error detecting memory module and method	SO, BYUNG-SE
<u>11227225</u>	Not Issued	30	09/16/2005	Method of testing memory module and memory module	SO, BYUNG-SE
<u>11256580</u>	Not Issued	25	10/21/2005	Determining operation mode for semiconductor memory device	SO, BYUNG-SE
<u>11266428</u>	Not Issued	30	11/04/2005	Memory module with stacked semiconductor devices	SO, BYUNG-SE
<u>60267908</u>	Not Issued	159	02/09/2001	Memory system having stub bus configuration	SO, BYUNG-SE
<u>60579657</u>	Not Issued	159	06/16/2004	Method of testing memory module in transparent mode and hub of memory module of testing the same	SO, BYUNG-SE
<u>11229609</u>	Not Issued	20	09/20/2005	Apparatus and method of providing fingertip haptics of visual information using electro-active polymer for image display device	SOH, BYUNG SEOK
<u>11291927</u>	Not Issued	20	12/02/2005	Method and apparatus for controlling input/output interface	SOH, BYUNG SEOK
<u>11206076</u>	Not Issued	30	08/18/2005	Line-of-sight-based authentication apparatus and method	SOH, BYUNG SEOK
<u>10455434</u>	Not Issued	71	06/06/2003	Method of and apparatus for inputting character using pointing device	SOH, BYUNG-SEOK
<u>10540925</u>	Not Issued	25	05/12/2006	Method for configuring 3d input device, method for reconfiguring 3d input device, method for recognizing wearing of the 3d input device, and the apparatus thereof	SOH, BYUNG-SEOK
<u>10735906</u>	Not Issued	41	12/16/2003	Apparatus and method for detecting finger-motion	SOH, BYUNG-SEOK
<u>11043423</u>	Not Issued	30	01/27/2005	Method of adjusting pointing position during click operation and 3D input device using the same	SOH, BYUNG-SEOK
<u>11183171</u>	Not Issued	20	07/18/2005	Apparatus and method for providing haptics of image	SOH, BYUNG-SEOK

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Search Another: Inventor	<input type="text" value="so"/>	<input type="text" value="byung"/>	<input type="button" value="Search"/>

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**PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = LEE

First Name = JAE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08955378</a>	<a href="#">5939954</a>	150	10/21/1997	EQUIVALENT CIRCUIT OF PACKAGE GROUND TERMINAL PADDLE	LEE, JAE
<a href="#">09080326</a>	<a href="#">6046659</a>	150	05/15/1998	DESIGN AND FABRICATION OF BROADBAND SURFACE-MICROMACHINED MICRO-ELECTRO-MECHANICAL SWITCHES FOR MICROWAVE AND MILLIMETER-WAVE APPLICATIONS	LEE, JAE
<a href="#">09531874</a>	Not Issued	161	03/21/2000	Magnetic point massager	LEE, JAE A.
<a href="#">08227234</a>	Not Issued	161	04/13/1994	MINI ACCU-PRESSURE MASSAGER	LEE, JAE A.
<a href="#">29021250</a>	<a href="#">D369414</a>	150	04/13/1994	ACCU-PRESSURE MASSAGER	LEE, JAE A.
<a href="#">29021256</a>	<a href="#">D361132</a>	150	04/13/1994	ACCU-PRESSURE MASSAGER	LEE, JAE A.
<a href="#">29116123</a>	<a href="#">D430938</a>	150	12/28/1999	MAGNETIC POINT MASSAGER	LEE, JAE A.
<a href="#">06935326</a>	Not Issued	161	03/03/1987	"SET-UP" CAN	LEE, JAE B.
<a href="#">06941189</a>	Not Issued	161	12/12/1986	METHODIZED BOLT, NUT, SCREW AND SCREW-DRIVER	LEE, JAE B.
<a href="#">07012152</a>	Not Issued	161	04/28/1987	EMERGENT HONK FOR TURN	LEE, JAE B.
<a href="#">07017148</a>	Not Issued	161	04/28/1987	WATCHER TELEVISION	LEE, JAE B.
<a href="#">07097837</a>	Not Issued	161	09/17/1987	METHODIZED BOLT, NUT, AND SCREW	LEE, JAE B.
<a href="#">07097838</a>	Not Issued	161	09/17/1987	METHODIZED SCREWDRIVER	LEE, JAE B.

<u>07147245</u>	Not Issued	161	02/04/1988	LENS FOR CORRECTING THE IMAGES COME FROM FOCUSING CONCAVE LENS	LEE, JAE B.
<u>07320573</u>	Not Issued	169	03/08/1989	SCREW-TYPE FASTENING DEVICE.A.	LEE, JAE B.
<u>07320618</u>	Not Issued	169	03/08/1989	SCREW TYPE FASTENING DEVICE WITH DOUBLE SPIRALS	LEE, JAE B.
<u>07005221</u>	Not Issued	161	03/10/1987	FOCUSING CONCAVE LENS, TELEPHOTO LENS, AND CAMERA LENS FOR PHOTOGRAPHING AND SEARCHING	LEE, JAE BEOM
<u>07186352</u>	<u>4810149</u>	150	04/26/1988	SCREW-TYPE FASTENING DEVICE	LEE, JAE BEOM
<u>07186389</u>	<u>4850775</u>	150	04/26/1988	SCREW-TYPE FASTENING DEVICE	LEE, JAE BEOM
<u>10565255</u>	Not Issued	30	01/19/2006	Silicon film bulk acoustic wave device and process of the same	LEE, JAE BIN
<u>10182244</u>	Not Issued	161	05/23/2003	Method for manufacturing a shadow mask	LEE, JAE BOK
<u>11023620</u>	Not Issued	93	12/29/2004	THIN FILM TRANSISTOR SUBSTRATE OF A HORIZONTAL ELECTRIC FIELD TYPE AND METHOD OF DARKENING DEFECTIVE PIXEL IN THE SAME	LEE, JAE BONG
<u>11451121</u>	Not Issued	19	06/12/2006	Submerged-type electrosorption-based water purification apparatus and method thereof	LEE, JAE BONG
<u>10667662</u>	Not Issued	30	09/23/2003	Method and apparatus for displaying digital broadcasting	LEE, JAE BUNM
<u>06932876</u>	Not Issued	161	11/20/1986	RINGPOLE	LEE, JAE C.
<u>07453594</u>	Not Issued	161	12/20/1989	PIEZOELECTRIC CERAMIC COMPOSITION	LEE, JAE C.
<u>07736035</u>	<u>5099379</u>	150	01/25/1991	SURGE PREVENTING CIRCUIT FOR ELECTROMAGNETIC INDUCTION COOKING APPARATUS	LEE, JAE C.
<u>07745347</u>	<u>5218266</u>	250	08/15/1991	COLOR PICTURE TUBE HAVING RESIDUE ELECTRON REMOVING ASSEMBLY	LEE, JAE C.
<u>08166864</u>	<u>5405965</u>	150	12/15/1993	PROCESSES FOR PREPARATION OF 5-	LEE, JAE C.

				PYRAZOLEMERCAPTAN DERIVATIVES AND INTERMEDIATES THEREOF	
<u>08233432</u>	Not Issued	161	04/28/1994	METHOD FOR THE PREPARATION OF THERMAL RESISTANT AND CHLORINE RESISTANT ELASTIC FIBER	LEE, JAE C.
<u>08341893</u>	<u>5416220</u>	150	11/15/1994	PROCESSES FOR PREPARATION OF 5- PYRAZOLEMERCAPTAN DERIVATIVES	LEE, JAE C.
<u>08350979</u>	<u>5534637</u>	150	11/29/1994	STARTING MATERIALS FOR PREPARATION OF SULFONYLUREA DERIVATIVES AND PROCESS FOR PREPARING THE SAME	LEE, JAE C.
<u>08355197</u>	<u>5509283</u>	150	12/08/1994	CLOTHES WASHER HAVING WATER RECIRCULATION SYSTEM	LEE, JAE C.
<u>08377642</u>	<u>5648129</u>	150	01/25/1995	MELODIC PARTY-FAVORS	LEE, JAE C.
<u>08386825</u>	<u>5468896</u>	150	02/10/1995	DITHIOCARBAZATE COMPOUNDS	LEE, JAE C.
<u>08386867</u>	<u>5466854</u>	150	02/10/1995	PROCESSES FOR PREPARATION OF DITHIOCARBAZATES	LEE, JAE C.
<u>08389042</u>	<u>5525730</u>	150	02/15/1995	PROCESSES FOR PREPARATION OF SULFONYLUREA DERIVATIVES	LEE, JAE C.
<u>08534981</u>	Not Issued	161	09/27/1995	PROCESS FOR THE PREPARATION OF VISCOSITY- STABLE POLYURETHANIC ELASTOMER	LEE, JAE C.
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